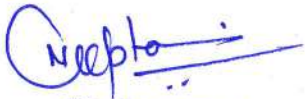
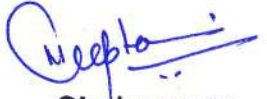



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
M.Tech. in Embedded System (w.e.f. July 2020)													
I - SEMESTER													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	MTMAEC101	Advanced Mathematics		3	1	0	60	20	20	0	0	100	4
2	MTES101	Digital VLSI Design		3	0	2	60	20	20	30	20	150	4
3	MTES102	Microcontrollers and Interfacing		3	0	2	60	20	20	30	20	150	4
4	MTES113/ MTES123/ MTDC203	Elective: 1 HDL Fundamentals 2 Robotics and Machine Vision 3 Advanced Communication Systems		2	0	2	60	20	20	30	20	150	3
5	MTDC101/ MTES114/ MTES124	Elective: 1 Data Communication and Computer Networks 2 Data Acquisition System 3 Embedded System Designing using Raspberry pi		2	0	2	60	20	20	30	20	150	3
TOTAL				13	1	8	300	100	100	120	80	700	18
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Chairperson
Faculty of Studies
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, INDORE


Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore


Controller of Examinations
 Shri Vaishnav Vidyapeeth Vishwavidyalaya


Vice Chancellor
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore